AMENDMENTS TO THE CLAIMS

1. (previously presented) A method of speculatively issuing memory requests while maintaining a specified packet order comprising:

receiving a first packet and a second packet for forwarding, wherein said first packet is received prior to said second packet;

sending a first memory request corresponding to said first packet; sending a second memory request corresponding to said second packet prior to receiving a first memory reply corresponding to said first memory request;

forwarding said first packet prior to forwarding said second packet; and receiving a second memory reply prior to forwarding said first packet.

- 2-3. (cancelled)
- 4. (original) The method as described in Claim 1 wherein said first memory request is to request resource to forward said first packet.
 - 5. (cancelled)
- 6. (previously presented) The method as described in Claim 1 further comprising receiving said first memory reply prior to forwarding said first packet.
- 7. (original) The method as described in Claim 1 wherein said first packet comprises an internet protocol packet.
 - 8. (previously presented) A network method comprising:

 receiving a first packet and a second packet for forwarding;

 sending a first memory request corresponding to said first packet;

 sending a second memory request corresponding to said second packet

 prior to forwarding said first packet; and

receiving a second memory reply prior to forwarding said first packet.

Serial No.: 10/091,775 Examiner: Wong, Warner
- 2 - Art Unit: 2668

9-10. (cancelled)

- 11. (original) The method as described in Claim 8 wherein said first memory request is to request resource to forward said first packet.
 - 12. (cancelled)
- 13. (original) The method as described in Claim 8 wherein said first memory reply is received prior to forwarding said first packet.
- 14. (original) The method as described in Claim 8 wherein said first packet comprises an internet protocol packet.
 - 15. (previously presented) A networking device comprising: a first packet processor comprising:

an input interface having a port to accept incoming packets;

an input memory coupled to said input interface for temporarily storing said packets in a queue arranged by a receiving order;

a second packet processor comprising:

an output interface having a port to send said packets out of said networking device;

an output memory coupled to said output interface for temporarily storing said packets;

a switching fabric coupled to said first packet processor and said second packet processor for conveying information between said first packet processor and said second packet processor; and

said first packet processor also for sending a memory request corresponding to a first packet which is not at a head of said queue to said second packet processor, said first packet processor also for receiving a memory reply from said second packet processor corresponding to said

Serial No.: 10/091,775 Examiner: Wong, Warner - 3 -

memory request for said first packet prior to forwarding a second packet that is ahead of said first packet in said queue.

16. (cancelled)

- 17. (currently amended) The networking device as described in Claim 16 15 wherein said first packet processor is also for sending a third packet to said second packet processor, wherein said third packet is at the head of said queue.
- 18. (previously presented) The networking device as described in Claim 17 wherein said second packet processor is also for receiving said first packet, said second packet and said third packet.
- 19. (Previously Presented) The networking device as described in Claim 15 wherein said second packet processor is also for sending said first packet out of said networking device.
- 20. (original) The networking device as described in Claim 15 further comprising a plurality of packet processors in addition to said first and said second packet processors coupled to said switching fabric.
- 21. (Previously Presented) The networking device as described in Claim 15 wherein said memory request comprises a first portion to indicate that said first packet is not at a head of said queue.
- 22. (Previously Presented) The networking device as described in Claim 15 wherein said first packet is an internet protocol packet.
 - 23. (previously presented) A networking device comprising: a means for sending a memory request corresponding to a second packet prior to sending a first packet, wherein said first packet is received prior to receiving said second packet; and

Serial No.: 10/091,775 Examiner: Wong, Warner Art Unit: 2668

a means for receiving a memory reply corresponding to said memory request prior to sending said first packet.

24-26. (cancelled)

27. (original) The networking device as described in Claim 23 wherein said means for sending a memory request further comprises means to request resource to transfer said packet.

28. (original) The networking device as described in Claim 23 wherein said means for sending a memory request further comprises means for accepting a memory reply prior to forwarding said packet.

29. (original) The networking device as described in Claim 28 wherein said means for accepting a memory reply further comprises means to assign network resource to transfer said packet.

30. (original) The networking device as described in Claim 23 wherein said packet is an internet protocol packet.

31. (previously presented) The method as described in Claim 1 wherein said first packet and said second packet are maintained in a transfer order queue.

32. (previously presented) The method as described in Claim 31 wherein said second memory request is sent prior to said second packet moving to a head of said transfer order queue.

33. (previously presented) The method as described in Claim 8 wherein said first packet and said second packet are maintained in a transfer order queue.

Serial No.: 10/091,775 Examiner: Wong, Warner

- 34. (previously presented) The method as described in Claim 33 wherein said second memory quest is sent prior to said second packet moving to a head of said transfer order queue.
- 35. (previously presented) The networking device as described in Claim 23 wherein said means for sending a memory request further comprises means for maintaining the transfer order of said first and said second packets.
- (previously presented) The networking device as described in Claim 35 36. wherein said means for maintaining the transfer order of said first and said second packets comprises a transfer order queue.
- 37. (currently amended) The networking device as described in Claim 25 23 wherein said means for sending a memory request further comprise sending said memory request for said second packet prior to said second packet reaching a head of said transfer order queue.

Serial No.: 10/091,775 Examiner: Wong, Warner Art Unit: 2668 - 6 -

CONCLUSION

Applicants respectfully submit that the above-presented amendments are in compliance with 37 CFR 1.121. Applicants therefore respectfully request reconsideration of the rejected claims based on the arguments presented in the response filed November 7, 2006. Applicants respectfully assert that Claims 1, 4, 6-8, 11, 13-14, 23, and 27-37 overcome the rejections of record and, therefore, Applicants respectfully solicit allowance of these Claims. The Examiner is invited to contact Bill O'Meara at 970-898-7917 if the Examiner believes such action would expedite resolution of the present Application.

Respectfully submitted,

WAGNER, MURABITO & HAO L.L.P.

Dated: 12/7/06

James P. Hao

Registration No. 36,398

Two North Market Street Third Floor San Jose, CA 95113 (408) 938-9060

Serial No.: 10/091,775 Examiner: Wong, Warner - 7 -

Art Unit: 2668